

<b>Notice of References Cited</b>	Application/Control No. 10/604,836	Applicant(s)/Patent Under Reexamination LEE, SZETSEN STEVEN	
	Examiner Eric B. Chen	Art Unit 1765	Page 1 of 1

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	B	US-6,197,116	03-2001	Kosugi, Makoto	118/712
	C	US-6,733,618	05-2004	Kagoshima et al.	156/345.24
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	K	US-			
	L	US-			
	M	US-			

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**NON-PATENT DOCUMENTS**

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	U	Wolf et al., Silicon Processing for the VLSI Era, 1986, Lattice Press, Vol. 1, pp. 546-47.
	V	Wolf, Silicon Processing for the VLSI Era, 1992, Lattice Press, Vol. 2, pp. 51-52, 570.
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.